AMENDMENTS TO THE CLAIMS:

This listing of claims replaces all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently Amended) A circuit configured for use with mobile wireless systems with different frequency bands, the circuit comprising:

an antenna connection,

a first signal path electrically connected to the antenna connection and having an assigned first frequency band, the first signal path comprising:

a first output terminal that is configured to connect to at least one secondary stage circuit; and

a first band-pass filter between the antenna connection and the first output terminal and connected directly to the antenna connection, the first band-pass filter comprising thin-layer resonators; and

a second signal path electrically connected to the antenna connection and in parallel with the first signal path, the second signal path having an assigned second frequency band that is different from the first frequency band, the second signal path comprising:

a second output terminal configured to connect to at least one secondary stage circuit; and

a second band-pass filter between the antenna connection and the second output terminal and connected directly to the antenna connection, the second band-pass filter comprising thin-layer resonators; and

a balun in at least one of the first and second signal paths.

2. (Canceled)

3. (Currently Amended) The circuit of claim 1 ~~2~~, wherein the balun is connected in at least one of the first and second signal paths between [[a]]the band-pass filter thereof and [[an]] a corresponding output terminal.

4. (Cancelled)

5. (Previously Presented) The circuit of claim 1, wherein thin-layer resonators in each of the first and second band-pass filters are stacked to form a compound resonator.

6. (Currently Amended) The circuit of claim 1, wherein each ~~of the first and the second~~ frequency band is separated from another frequency band by ~~the respective other one with a selectivity of~~ at least about 20 dB.

7. (Currently Amended) The circuit of claim 1, further comprising a duplexer in at least one ~~on~~ of the first and second signal paths, wherein the first signal path comprises a [[a]] first reception path and a first transmission path and the second signal path comprises a second reception path and a second transmission path.

8. (Previously Presented) The circuit of claim 7, further comprising a low noise amplifier (LNA) in at least one of the first and second signal paths, the LNA being downstream from the duplexer in a direction of signal propagation.

9. (Currently Amended) The circuit of claim 8, ~~further comprising~~ further comprising a power amplifier in at least one of the first and second signal paths, the power amplifier being ~~downstream~~ upstream from the duplexer in a direction of signal propagation.

10. (Previously Presented) The circuit of claim 1, wherein at least one of the first and the second signal paths is configured to conduct a symmetrical signal.

11. (Previously Presented) The circuit of claim 1, wherein the thin-layer resonators are acoustically coupled to form a compound resonator.

12. (Canceled)

13. (Currently Amended) Circuitry comprising:

an antenna;

a first duplexer connected directly to the antenna, the first duplexer comprising:

a first band-pass filter directly connected to the antenna;

a second band-pass filter; and

a first quarter-lambda ($\lambda/4$) line between the antenna and the second band-pass filter;

a second duplexer connected directly to the antenna, the second duplexer comprising:

a third band-pass filter directly connected to the antenna;

a fourth band-pass filter; and

a second quarter-lambda ($\lambda/4$) line between the antenna and the fourth band-pass filter;

a first signal path comprising a first transmission path and a first reception path;[[,]]

the first transmission path comprising:

a first input terminal; and

a ~~first~~ fifth band-pass filter between the first duplexer and the first input terminal;

the first reception path comprising:

a first output terminal; and

a ~~second~~ sixth band-pass filter between the first duplexer and the first output terminal; and

a second signal path comprising a second transmission path and a second reception path;

the second transmission path comprising:

a second input terminal; and

a ~~third~~ seventh band-pass filter between the second duplexer and the second input terminal;

the second reception path comprising:

a second output terminal; and

~~a fourth~~ an eighth band-pass filter between the second duplexer and the second output terminal.

14. (Currently Amended) The circuitry of claim 13, further comprising:

a first low noise amplifier (LNA) between the first duplexer and ~~first~~ sixth band-pass filter; and

a second low noise amplifier (LNA) between the first duplexer and ~~third~~ eighth band-pass filter.

15. (Currently Amended) The circuitry of claim 14, further comprising:

a ~~third low noise amplifier (LNA)~~ first power amplifier between the first duplexer and ~~second~~ fifth band-pass filter; and

a ~~fourth low noise amplifier (LNA)~~ second power amplifier between the ~~first~~ second duplexer and ~~fourth~~ seventh band-pass filter.

16. (New) Circuitry comprising:

an antenna;

a first duplexer connected directly to the antenna;

a second duplexer connected directly to the antenna;

a first signal path comprising a first transmission path and a first reception path;

the first transmission path comprising:

a first input terminal; and

a first band-pass filter between the first duplexer and the first input terminal;

the first reception path comprising:

a first output terminal; and

a second band-pass filter between the first duplexer and the first output terminal; and

a second signal path comprising a second transmission path and a second reception path;

the second transmission path comprising:

a second input terminal; and

a third band-pass filter between the second duplexer and the second input terminal;

the second reception path comprising:

a second output terminal; and

a fourth band-pass filter between the second duplexer and the second output terminal,

wherein at least one of the transmission and receptions paths is configured to conduct a symmetrical signal, and wherein balun functionality is integrated into at least one of the first and second duplexers.

17. (New) The circuit of claim 1, wherein at least one of the first and second band-pass filters is constructed to implement balun functionality.

18. (New) The circuit of claim 1, wherein one of the first and second signal paths comprises a first reception path, and wherein the balun is in the first reception path and comprises a unsymmetrical input and a symmetrical output.